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Curriculum vitae

PERSONAL INFORMATION	András Gerecs

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Nationality Hungarian

POSITION	Electrical engineer

WORK EXPERIENCE

Nov 2014–Present

Electrical engineer

Centre for Energy Research, Space Research Department (formerly MTA Centre for Energy Research, Radiation Protection Department, Space Dosimetry Research Group), Budapest (Hungary)

- **Electrical Systems Engineering and Hardware Development for the D3S-RADMAG instrument** under the D3S-SS project of ESA (European Space Agency) (2019 -)
- **Electrical Systems Engineering, Hardware, FPGA and Software Development for the RadMag instrument** for the RADCUBE IOD CubeSat mission (2016 -)
- **Hardware and Software Engineering for the ESEO-TRITEL satellite research mission** under the ESEO (European Student Earth Orbiter) project of ESA (European Space Agency) (2014 - 2018)
- **Hardware and Software Engineering for the REXUS-REMRED rocket research mission** under the REXUS/BEXUS (Rocket and Balloon Experiments for University Students) 17&18 project of ESA (European Space Agency) (2014 - 2015)

Apr 2009–Sep 2014

Electrical engineer, research and development

Budapest University of Technology and Economics, Department of Electronics Technology, Budapest (Hungary)

EDUCATION AND TRAINING

2004–2010

Degree in Electrical Engineering

Budapest University of Technology and Economics, Budapest (Hungary)

PERSONAL SKILLS

Mother tongue(s)

Hungarian

Foreign language(s)

	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken interaction	Spoken production	
English	C1	C2	B2	B2	C1
German	A2	B2	A1	A2	B1

Levels: A1 and A2: Basic user - B1 and B2: Independent user - C1 and C2: Proficient user
Common European Framework of Reference for Languages

Job-related skills

- analogue and digital circuit design

- switch mode power supply design
- electrical circuit simulation (LTspice, TINA)
- PCB design (Altium Designer)
- embedded programming (bare metal, C)
- HDL (Verilog)
- 8-bit (PIC16/18, ATmega, Silabs 8051) and 32-bit (STM32, SmartFusion2) microcontroller applications
- FPGA/CPLD applications (Lattice, Microsemi)